



NATIONAL INSTITUTE OF TECHNOLOGY RAIPUR

(Institute of National Importance)

G.E. Road, Raipur – 492010 (C.G)

Department of Electrical Engineering

Telephone : +91-771-2254200

Fax : +91-771-2254600 Email:

director@nitrr.ac.in

www.nitrr.ac.in

ADVERTISEMENT NO.: NITRR/Electrical/CPRI Project/2016/1

Raipur, Dated: 27.04.2016

Applications in the prescribed format are invited for the post of **Junior Research Fellow (JRF)** for a period of **Two years** (Initially for one year) as per the following requirements. Interested and eligible candidates can attend the **Walk-in-Interview** on **10/05/2016 at 11.30 A.M.** in the Department of Electrical Engineering along with filled applications and necessary original certificates.

Name of the Position	Junior Research Fellow (JRF)
Project Title	Studies to Improve the Performance of Fault Location Algorithm for Multi-Location Shunt Faults in Transmission Line: A Case study of Chhattisgarh State.
Principal Investigator	Dr. (Mrs.) Anamika Yadav (PI), Assistant Professor Department of Electrical Engineering NIT Raipur, Chhattisgarh- 492010 Email: ayadav.ele@nitrr.ac.in
Funding Agency	Central Power Research Institute (CPRI), Bangalore
Tenure of Scheme & Assignment	2 Years (Initially for one year) and performance will be reviewed on yearly basis for continuation.
Consolidated monthly fellowship	Rs. 30,000/- (fixed) per month (no HRA)
Essential qualifications	M.E/M.Tech in Integrated Power System/ Power system and control/ Power Electronics & Drives/Computer Technology with NET/GATE qualification. or B.E/B.Tech in Electrical Engg./Electrical & Electronics Engg./Electronics and Communication Engg./Electronics & Telecommunication/Computer Science/Information Technology and M.Tech Final year (IV SEM) candidates who have submitted their thesis; waiting for final results may also apply). Note: Selected candidate will be encouraged to apply for Ph.D.
Date and Time of Interview	10/05/2016 at 11.30 AM in Department of Electrical Engineering, NIT, Raipur

Terms and Conditions:

1. The above position is purely temporary and Continuation of the fellowship is subject to satisfactory performance and review after each year.
2. Candidates are required to appear for the interview with photocopy of the application form, curriculum vitae, and all relevant original certificates with a set of self-attested photocopies of the certificates.
3. No TA/DA is permissible for appearing for interview.
4. Selected candidates will not be permitted to claim for any regular/part time appointments in this institute or any other institutes during this period.
5. Canvassing in any form will disqualify the candidature and the appointment shall be co-terminus with the project.

For further details please contact:

Dr. Anamika Yadav (PI),

Assistant Professor, Department of Electrical Engineering

NIT Raipur, Chhattisgarh- 492010

Email: ayadav.ele@nitrr.ac.in, anamika_jugnu@yahoo.com, anamikajugnu4@gmail.com

Contact No: 9425852654

Copy to Webmaster to upload the advertisement in the institute website



NATIONAL INSTITUTE OF TECHNOLOGY RAIPUR

(Institute of National Importance)

G.E. Road, Raipur – 492010 (C.G)

Department of Electrical Engineering

Telephone : +91-771-2254200

Fax : +91-771-2254600 Email:

director@nitrr.ac.in

www.nitrr.ac.in

APPLICATION FOR THE POST OF JUNIOR RESEARCH FELLOW

ADVT. NO. DATED

POST APPLIED FOR

TITLE OF PROJECT

Attach Passport
Size Self-attested
Photograph

1. NAME OF CANDIDATE (in Block letter)

2. Date of Birth*: _____
Age as on closing date of the application: Year ____ Month ____ Days ____

3. Category* : SC ____ ST ____ OBC ____ General ____

4. ADDRESS
(WITH CONTACT NUMBER & E.MAIL)

5. FATHER'S NAME

6. BIRTH PLACE

7. QUALIFICATION (HIGH SCHOOL ONWARDS)*
(ATTACH CERTIFICATES ALSO) INCLUDING,

Sl. No.	Name of Exam	Board/University	% Marks	Year	CGPA/Grade	Division	Remarks
1.							
2.							

8. WORK EXPERIENCE (IN DETAIL)
(Attach Certificate also)

9. COMPUTER KNOWLEDGE /DEGREE

10. PUBLICATION (If Any)

UNDERTAKING

I understand that it is a contractual engagement on purely temporary basis for the Project. I declare that the above particulars are correct to the best of my knowledge and belief. I understand that action can be taken against me if these are found to be incorrect.

Place: _____

Date: _____

SIGNATURE OF CANDIDATE

*Please enclose relevant self-attested certificates.