



Applications are invited for the post of one Junior Research Fellow for a period of three years (to be reviewed annually) as per the following requirements given below. Eligible candidates can attend the Walk-in interview on **10.12.2015** at **11 AM** in Electrical Engineering Department along with the completed applications and necessary certificates.

Name of the temporary post	: Junior Research Fellow
Research Project Title	: Hybrid Optimization Based fractional order fuel cell modeling and online parameter estimation with design of adaptive controller for integrated power converters.
Principal Investigator	: Dr. S. Ghosh Department of Electrical Engineering, NIT Raipur, Chhatishgarh-492010; Email: sghosh.ele@nitr.ac.in Mobile: +918225817389
Funding Agency	: Naval Research Board, DRDO
Tenure of the scheme	: Three years (from October 2015)
Tenure of the assignment	: Three years (Performance shall be reviewed on annual basis).
Job description	: Research related to the project work.
Consolidated monthly fellowship	: Rs. 25,000 for the first two years and Rs. 28,000 for the last year.
Essential qualifications	: M.E / M.Tech in Electrical Engineering with specialization in Control systems, power electronics, electrical drives, instrumentation or equivalent from recognized University/ Institute. Experience in working with MATLAB, optimization & power electronics is preferred.
Date and Time of Interview	: 10/12/2015 at 11 AM in Electrical Engineering Department.

Eligible persons may attend the walk-in interview along with application form, photocopies of self attested educational qualification, experience certificates, grade/mark sheet, publications and affixed with colored photographs etc.;

Terms and Conditions:

1. The above position is purely temporary and Continuation of the fellowship is subject to satisfactory performance and review after each year.

2. Candidates are required to appear for the interview with photocopy of the application form, curriculum vitae, and all relevant original certificates with a set of self-attested photocopies of the certificates.
3. No TA/DA is permissible for appearing for interview.
4. Selected candidates will not be permitted to claim for any regular/part time appointments in this institute or any other institutes during this period.
5. Canvassing in any form will disqualify the candidature.
6. The appointment shall be co-terminus with the project.
7. No HRA will be provided.
8. Leave will be admissible as per the norms of NIT Raipur permissible to project fellow

For further details please contact

Dr. S. Ghosh

Head & Principal Investigator

Department of Electrical Engineering

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राष्ट्रीय प्रौद्योगिकी संस्थान रायपुर

(पूर्व में शासकीय इंजीनियरिंग महाविद्यालय)

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NATIONAL INSTITUTE OF TECHNOLOGY RAIPUR

(Formerly Govt. Engineering College)

G.E. Road, Raipur - 492010 (CG)

Department of Electrical Engineering
Application for the post of Junior Research Fellow

ADVT. NO. _____ DATED _____
POST APPLIED FOR _____
TITLE OF PROJECT _____

1 NAME OF CANDIDATE (in Block letter):

2 DOB:

3 AGE AS ON THE DATE OF THE
APPLICATION: YEAR/MONTH/DAYS

4 ADDRESS (WITH CONTACT NUMBER
AND E.MAIL):

5 FATHER'S NAME:

6 QUALIFICATION (HIGH SCHOOL
ONWARDS)* (ATTACH CERTIFICATES
ALSO) specifying Name of Exam
Board/University % Marks/Year/
CGPA/Grade Division/ Remarks

7 WORK EXPERIENCE (IN DETAIL) (Attach
Certificate also):

8 COMPUTER KNOWLEDGE /DEGREE:

9 PUBLICATION* (If Any)

UNDERTAKING

I understand that it is a contractual engagement on purely temporary basis for the Project. I declare that the above particulars are correct to the best of my knowledge and belief. I understand that action can be taken against me if these are found to be incorrect.

Place : _____

Date : _____

SIGNATURE OF CANDIDATE

*Please enclose relevant self attested certificates

Attach
Latest Self Attested
Passport Size
Photograph